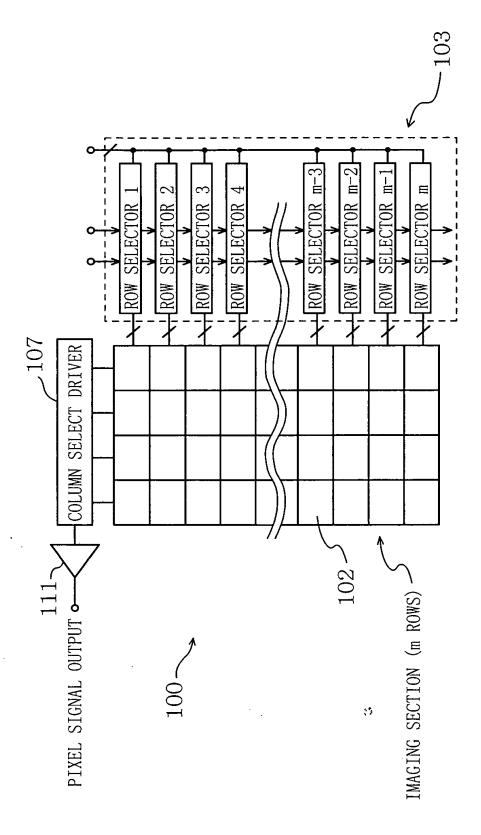
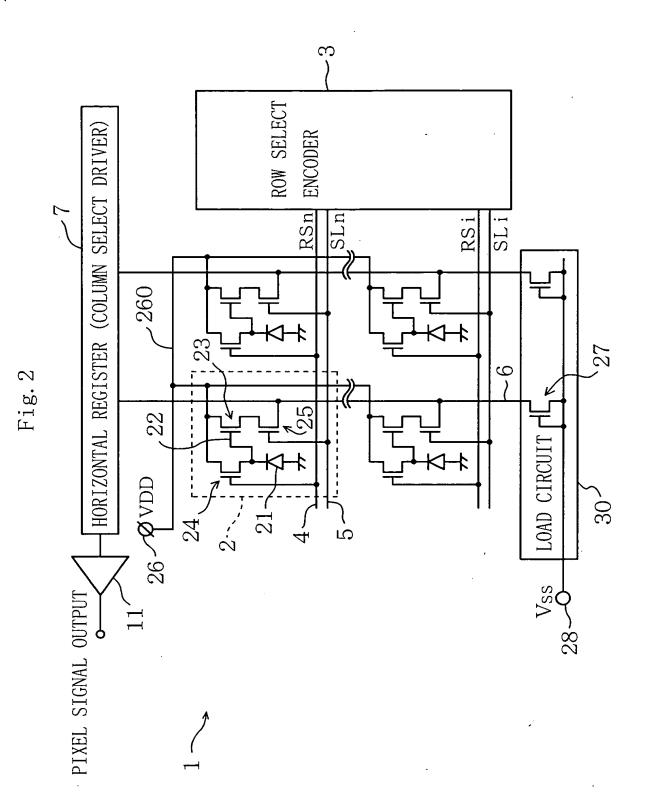
Fig. 1 PRIOR ART





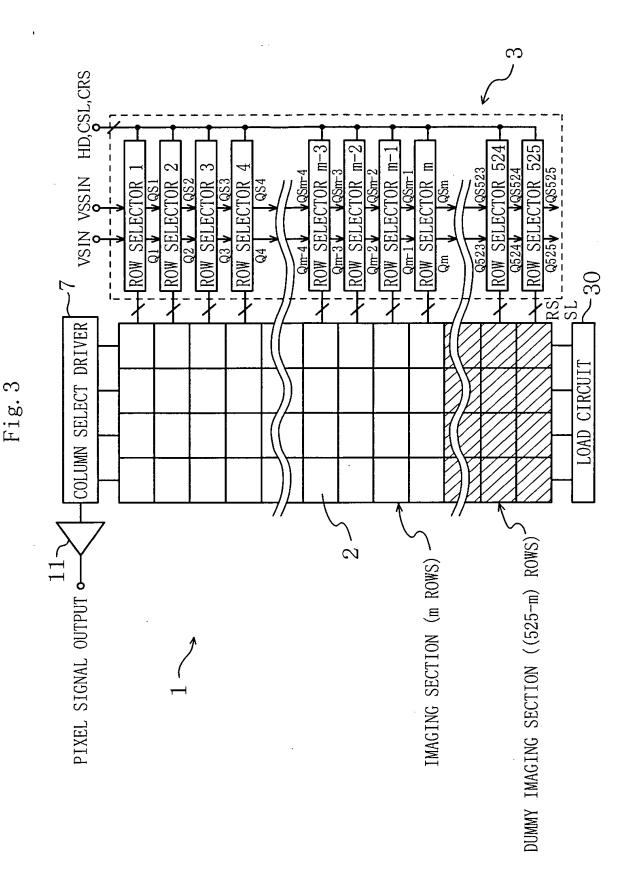
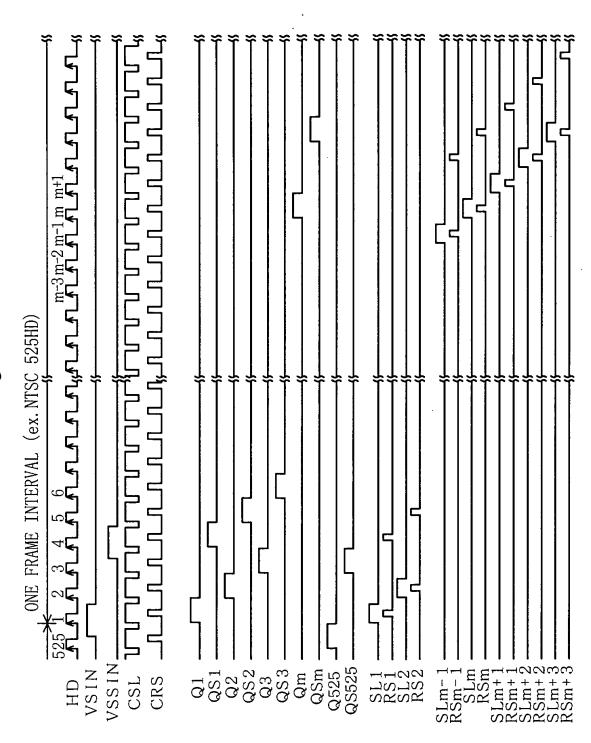


Fig. 4



The row SELECTOR - 50 CRS HD TO VSS IN - 50 TSO P QS i-1 QS i - 50 ~ 50 **△**VSIN <u>ක</u> RS i 🗘

Fig. 5

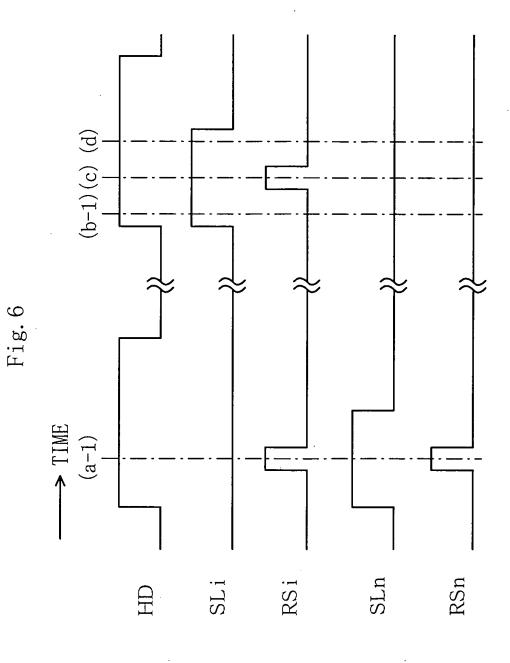
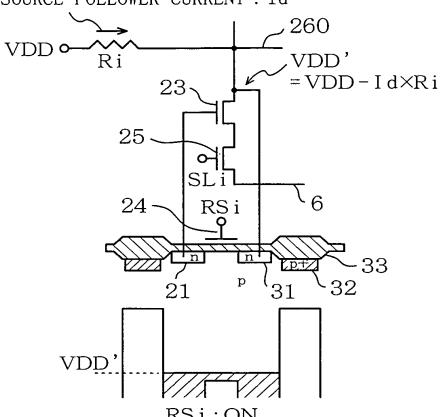


Fig. 7

 n^{th} ROW SOURCE FOLLOWER CURRENT : Id



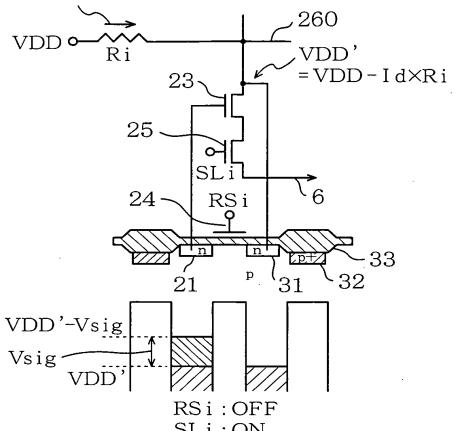
RSi:ON SLi:OFF

SLn: ON A ROW SELECT

TRANSISTOR TURNED ON

Fig. 8





SLi:ON

SLn:OFF

Fig. 9

 $i^{\,\text{th}}$ ROW SOURCE FOLLOWER CURRENT : Id

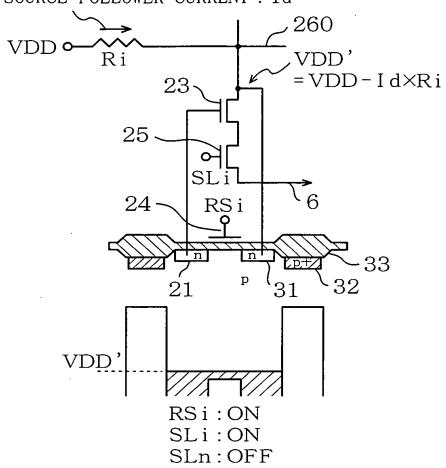
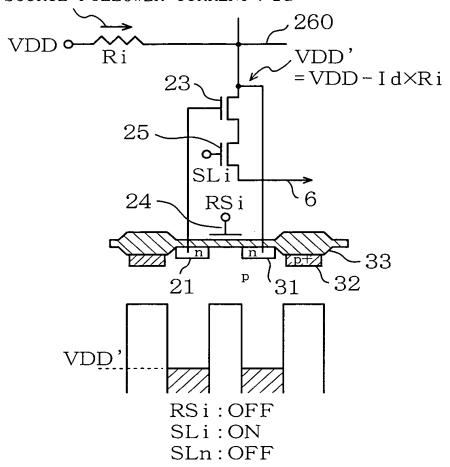
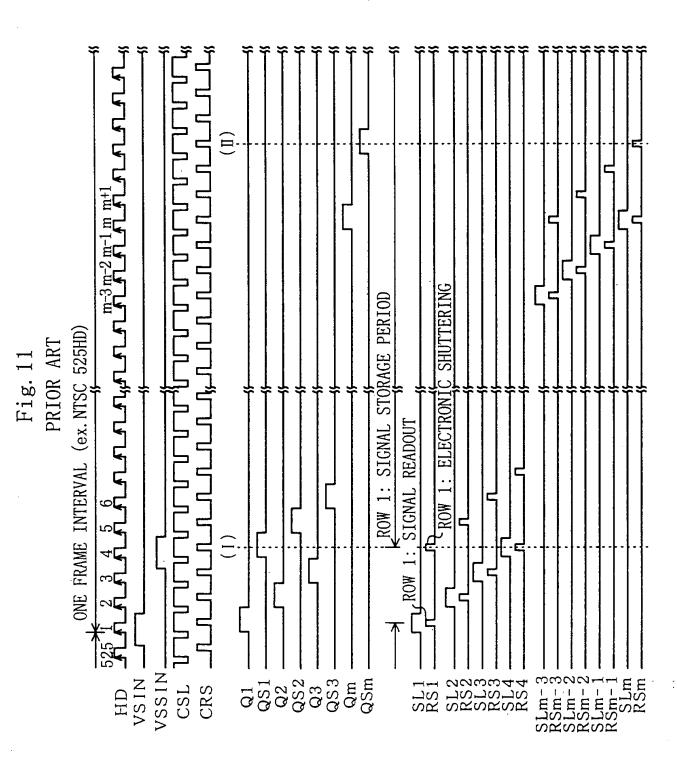


Fig. 10

ROW SOURCE FOLLOWER CURRENT : Id





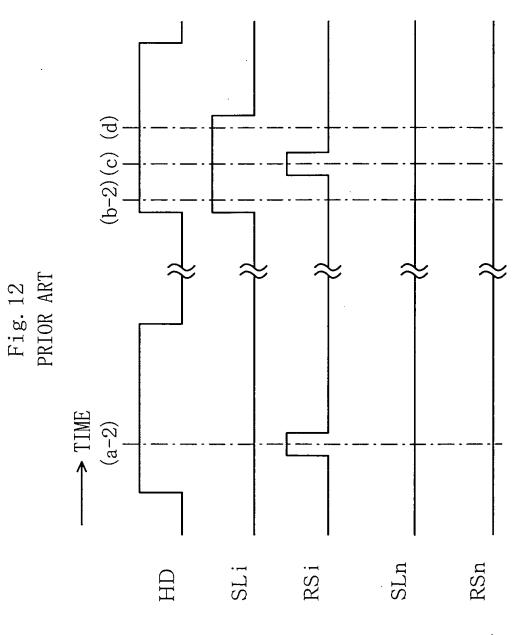
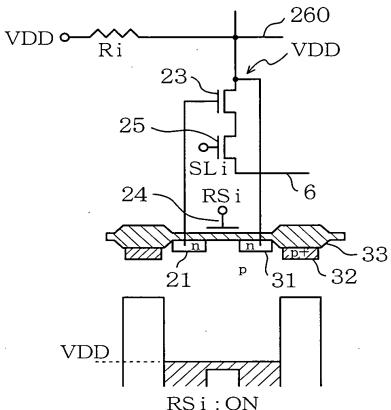


Fig. 13 PRIOR ART



RSi:ON

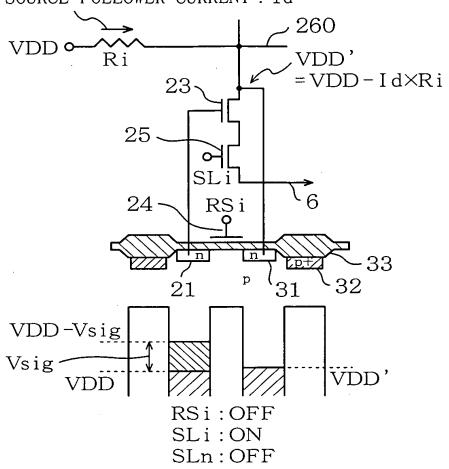
SLi:OFF

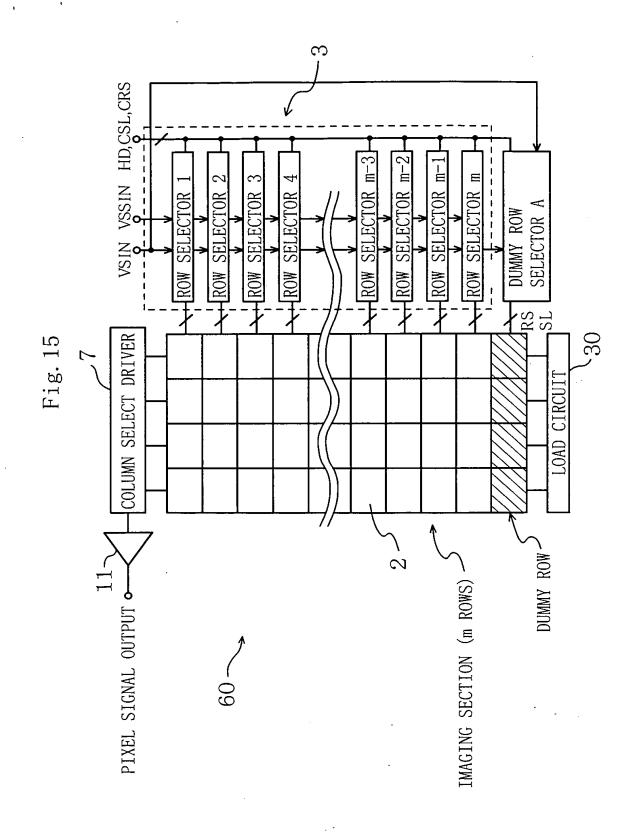
SLn:OFF

ALL ROW SELECT TRANSISTORS TURNED OFF

Fig. 14 PRIOR ART

 $i^{\, \text{th}}$ ROW SOURCE FOLLOWER CURRENT : Id





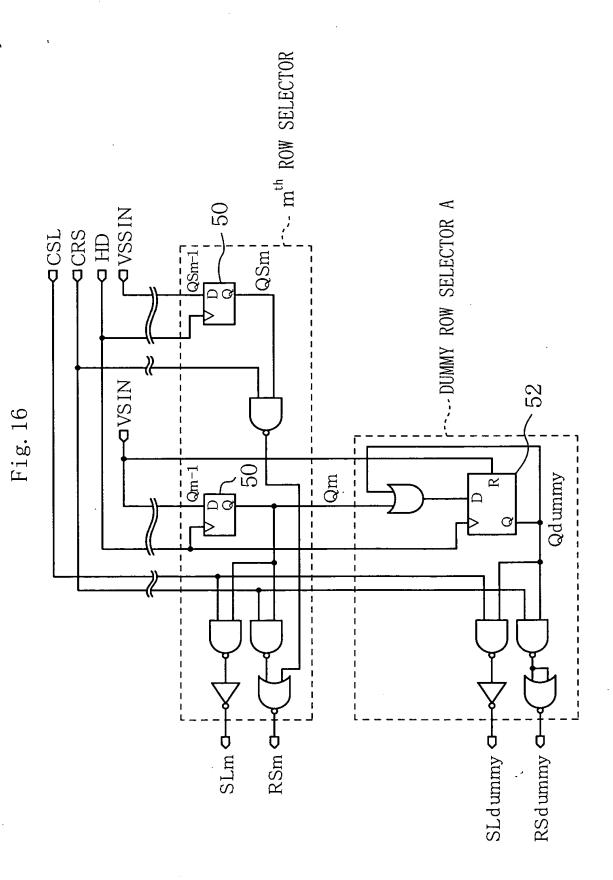
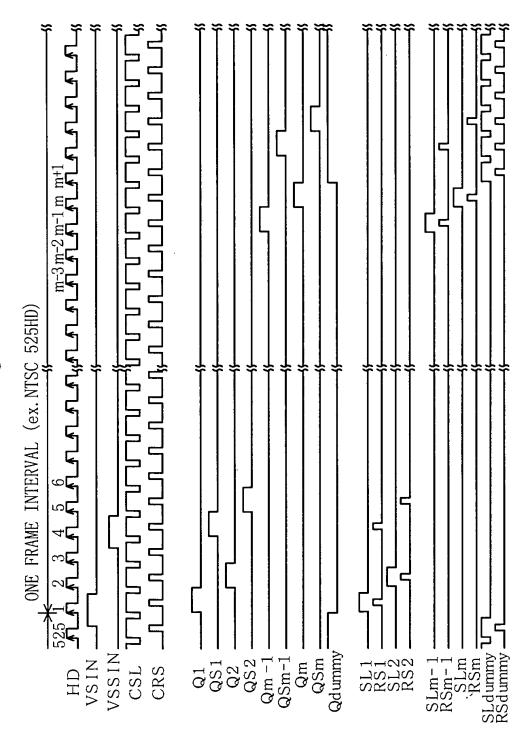
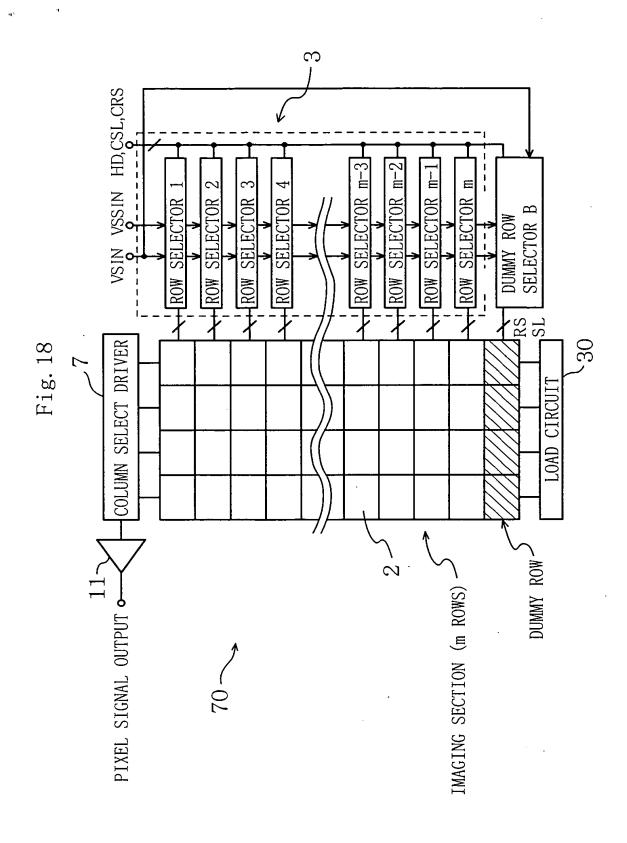


Fig. 17





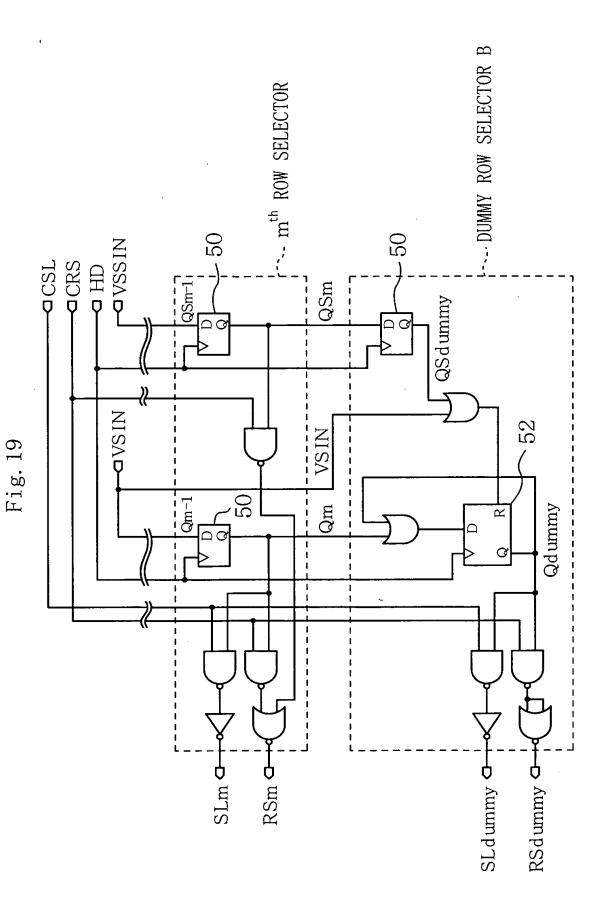


Fig. 20

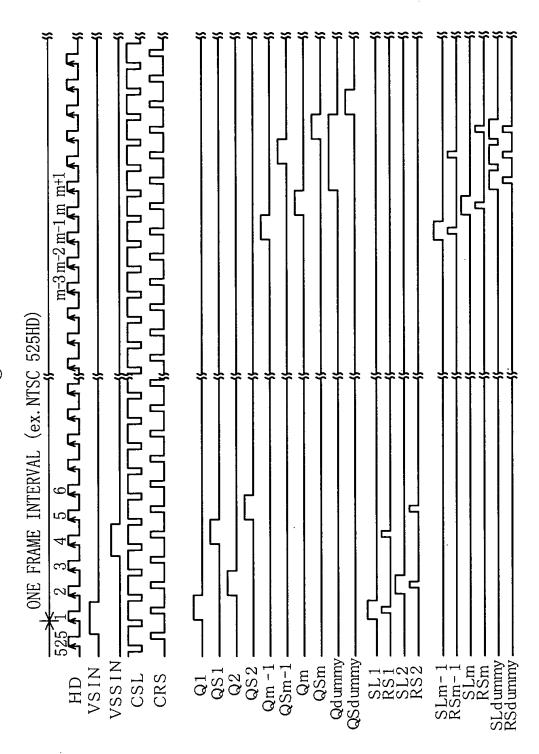


Fig. 21

